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Chapter 8 Problems

Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for ~~Digital Logic And Computer Design Chapter 1~~
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- Part 07 - Multiplexers Q. 2.14:
Implement the Boolean function $F = xy + x'y' + y'z$ (a) With AND, OR, and
inverter gates **Q. 3.16: Simplify the***

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**following functions, and implement
them with two-level NAND gate**

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