

Chapter 2 The Microprocessor And Its Architecture

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2.1. Internal Microprocessor Architecture . Before a program is written or any instruction investigated, the internal configuration of the microprocessor must be known. This section of the chapter details the program-visible internal architecture of the 8086—80486 and the Pentium—Pentium II microprocessors.

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Chapter 2 The Microprocessor And Its Architecture

The Arithmetic Logic Unit, or ALU is the part of the microprocessor that performs arithmetic operations. ALLUs can typically add, subtract, divide, multiply, and perform logical operations of two numbers (and, or, nor, not, etc). ALU will be discussed in far more detail in a later chapter, ALU. Registers

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Microprocessor can't directly understand programming languages, so programs have to be converted into ____ that corresponds to the microprocessor's instruction set. Machine language Modern refrigerators, washing machines, and other appliances are controlled by integrated circuits called ____ that combine sensors with processing circuitry.

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Keeping students on the forefront of technology, this text offers a practical reference to all programming and interfacing aspects of the popular Intel microprocessor family.

The textbook on microprocessors and microcontrollers has been developed as per the latest syllabus requirements of ECE, CSE & IT branches of engineering. Its lucid explanation and strong features such as design-based exercises, ample examples, review questions and assembly language programming examples lay a solid foundation for the subject.

The book provides comprehensive coverage of the hardware and software aspects of the 8085 microprocessor. It also introduces advanced processors from Intel family, SUN SPARC microprocessor and ARM Processor. The book teaches you the 8085 architecture, instruction set, machine cycles and timing diagrams, Assembly Language Programming (ALP), Interrupts, interfacing 8085 with support chips, memory and peripheral ICs - 8255 and 8259. The book explains the features, architecture, memory addressing, operating modes, addressing modes of Intel 8086, 80286, 80386 microprocessors, segmentation, paging and protection mechanism provided by 80386 microprocessor and the features of 80486 and Pentium Processors. It also explains the architecture of SUN SPARC microprocessor and ARM Processor.

This book provides the students with a solid foundation in the technology of microprocessors and microcontrollers, their principles and applications. It comprehensively presents the material necessary for understanding the internal architecture as well as system design aspects of Intel's legendary 8085 and 8086 microprocessors and Intel's 8051 and 8096 microcontrollers. The book throughout maintains an appropriate balance between the basic concepts and the skill sets needed for system design. Besides, the book lucidly explains the hardware architecture, the instruction set and programming, support chips, peripheral interfacing, and cites several relevant examples to help the readers develop a complete understanding of industrial application projects. Several system design case studies are included to reinforce the concepts discussed. With exhaustive coverage provided and practical approach emphasized, the book would be indispensable to undergraduate students of Electrical and Electronics, Electronics and Communication, and Electronics and Instrumentation Engineering. It can be used for a variety of courses in Microprocessors, Microcontrollers, and Embedded System Design.

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The book is written for an undergraduate course on the 16-bit, 32-bit and 64-bit Intel Processors. It provides comprehensive coverage of the hardware and software aspects of 8086, 80286, 80386, 80486 and Pentium Processors. The book uses plain and lucid language to explain each topic. The book provides the logical method of describing the various complicated concepts and stepwise techniques for easy understanding, making the subject more interesting. The book begins with an overview of microcomputer structure and operation, microprocessor evolution and types and the 8086 microprocessor family. It explains the 8086 architecture, instruction set, instruction timings, addressing modes, Assembly Language Programming (ALP), assembler directives, standard program structures in 8086 assembly language, machine coding for 8086 instructions, ALP program development tools, 8086 interrupts, PIC 8259 and interrupt applications. It focuses on features, architecture, pin description, data types, addressing modes and newly supported instructions of 80286 and 80386 microprocessors. It discusses various operating modes supported by 80386 - Real Mode, Protected Mode and Virtual 8086 Mode. Finally, the book focuses on multitasking, 80486 architecture and Pentium architecture. It describes Pentium superscalar architecture, pipelining, instruction pairing rules, instruction and data cache, floating-point unit and overview of Pentium II, Pentium III and Pentium IV processors.

Each topic is well explained by illustration and photographs. The book covers basic microprocessors to advanced processors in a consistent progression from theoretical concept to design considerations. The operation of various microprocessors is described with the help of pin diagram, functional diagram and timing diagrams. A large number of working programs, problem, and the each chapter are summarized in the end.

The 8085 Microprocessor: Architecture, Programming and Interfacing is designed for an undergraduate course on the 8085 microprocessor, this text provides comprehensive coverage of the programming and interfacing of the 8-bit microprocessor. Written in a simple and easy-to-understand manner, this book introduces the reader to the basics and the architecture of the 8085 microprocessor. It presents balanced coverage of both hardware and software concepts related to the microprocessor.

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